

**Amendments to the Claims:**

This listing of all pending claims (including withdrawn claims) will replace all prior versions, and listings, of claims in the application. Cancelled and not entered claims are indicated with claim number and status only. The claims show added text with underlining and deleted text with ~~strikethrough~~. The status of each claim is indicated with one of (original), (currently amended), (cancelled), (withdrawn), (new), (previously presented), or (not entered).

**Listing of Claims:**

1. (Currently Amended) A semiconductor memory having a function of protecting data stored in a nonvolatile semiconductor memory, the semiconductor memory comprising:
  - a volatile protection state specification section for controlling the protection state of data in the nonvolatile semiconductor memory; and
  - a nonvolatile initial state store section for controlling the initial state of the protection state specification section,  
wherein the protection state specification section is located for each of sectors in the nonvolatile semiconductor memory, further wherein the initial state store section is located for each of the protection state specification sections.
2. (Original) The semiconductor memory according to claim 1, wherein the protection state specification section includes one bit and determines the protection state of data by one or zero, further wherein the initial state store section includes one bit and determines the initial state by one or zero.
3. (Canceled)
4. (Original) The semiconductor memory according to claim 1, wherein the number of the initial state store section located for the plurality of the protection state specification sections is one.
5. (Original) The semiconductor memory according to claim 1, further comprising a volatile protection state lock section for locking the state of the protection state specification sections.

6. (Original) The semiconductor memory according to claim 5, wherein the protection state lock section includes one bit and determines by one or zero whether to lock the protection state of data.

7. (Original) The semiconductor memory according to claim 5, further comprising a second nonvolatile initial state store section for determining an initial state of the protection state lock section.

8. (Original) The semiconductor memory according to claim 7, wherein the second initial state store section includes one bit and determines by one or zero whether to lock the protection state of data.

9. (Original) The semiconductor memory according to claim 5, wherein a security level is divided into two stages on the basis of whether the protection state of data is locked by the protection state lock section, further wherein authentication with a password is performed to change the protection state of data from a locked state to a non-locked state.

10. (New) A semiconductor memory having a function of protecting data stored in a nonvolatile semiconductor memory, the semiconductor memory comprising:

    a volatile protection state specification section for controlling the protection state of data in the nonvolatile semiconductor memory; and

    a nonvolatile initial state store section for controlling the initial state of the protection state specification section,

    wherein the number of the initial state store section located for a plurality of the protection state specification sections is one.

11. (New) A semiconductor memory having a function of protecting data stored in a nonvolatile semiconductor memory, the semiconductor memory comprising:

    a volatile protection state specification section for controlling the protection state of data in the nonvolatile semiconductor memory;

    a nonvolatile initial state store section for controlling the initial state of the protection state specification section;

    a volatile protection state lock section for locking the state of the protection state specification section; and

a second nonvolatile initial state store section for determining an initial state of the protection state lock section.

12. (New) The semiconductor memory according to claim 11, wherein the second initial state store section includes one bit and determines by one or zero whether to lock the protection state of data.